

---

Posted by [seyko2](#) on Sun, 26 Oct 2008 14:33:34 GMT  
[View Forum Message](#) <> [Reply to Message](#)

---

BUG-report.

---

---

Posted by [seyko2](#) on Mon, 27 Oct 2008 01:41:51 GMT  
[View Forum Message](#) <> [Reply to Message](#)

---

/sys/devices/system/clocksource/clocksource0/available\_clocksource

---

---

Posted by [koct9i](#) on Mon, 27 Oct 2008 07:32:28 GMT  
[View Forum Message](#) <> [Reply to Message](#)

---

---

---

Posted by [seyko2](#) on Thu, 30 Oct 2008 23:36:56 GMT  
[View Forum Message](#) <> [Reply to Message](#)

---

<http://www.x86-secret.com/?option=newsd&nid=846>

clocksource

Until TSC becomes invariant, AMD recommends that operating system developers avoid TSC as a fast timer source on affected systems. (AMD recommends that the operating system should favor these time sources in a prioritized manner: HPET first, then ACPI PM Timer, then PIT.) The following pseudo-code shows one way of determining when to use TSC: