

---

Subject: Re: OpenVZ cpu / mem monitor

Posted by [jasonaward](#) on Sun, 01 Oct 2006 12:51:04 GMT

[View Forum Message](#) <> [Reply to Message](#)

---

Okay, I make a couple of modifications which should take care of the overflow I was seeing in the debug file and should correct any calculations.

The one thing i did notice from the debug file was this:

```
331 331 0 0 699 699 869323984949371 869346351937203
```

The first two are user\_jiffies (at previous check and currently), the second are nice\_jiffies, the third are system\_jiffies, and the fourth is the idle cycles counter. You'll notice in the debug file that over the 60 second run, it appears as though you consumed no user/nice/system jiffies. Though isn't completely shocking since the counter uses 22349MHz as your CPU total speed, but it would give an indication as to why you would be seeing 100% idle time. Try running this latest fix of the monitor while consuming CPU cycles and let me know if it works correctly now.

Thanks!

---