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Posted by [seyko2](#) on Thu, 30 Oct 2008 23:36:56 GMT

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<http://www.x86-secret.com/?option=newsd&nid=846>

clocksource

Until TSC becomes invariant, AMD recommends that operating system developers avoid TSC as a fast timer source on affected systems. (AMD recommends that the operating system should favor these time sources in a prioritized manner: HPET first, then ACPI PM Timer, then PIT.) The following pseudo-code shows one way of determining when to use TSC:

