
Subject: bogomips != bogomips in /proc/cpuinfo
Posted by [sspt](#) on Thu, 31 Jul 2008 15:47:13 GMT
[View Forum Message](#) <> [Reply to Message](#)

Hi,
I've recently reloaded a box with CentOS 5.2 + 2.6.18-53.1.19.el5.028stab053.14. There seems to be a problem with NMI watchdog and the bogomips is incorrect. Any idea about what can cause it?

```
/var/log/messages (First boot, 2.6.18-92.el5)
Jul 29 18:26:31 server kernel: Booting processor 1/4 APIC 0x1
Jul 29 18:26:31 server kernel: Initializing CPU#1
Jul 29 18:26:31 server kernel: Calibrating delay using timer specific routine.. 4386.89 BogoMIPS
(lpj=2193447)
Jul 29 18:26:31 server kernel: CPU: L1 I Cache: 64K (64 bytes/line), D cache 64K (64 bytes/line)
Jul 29 18:26:31 server kernel: CPU: L2 Cache: 1024K (64 bytes/line)
Jul 29 18:26:31 server kernel: CPU 1/1 -> Node 0
Jul 29 18:26:31 server kernel: CPU: Physical Processor ID: 0
Jul 29 18:26:31 server kernel: CPU: Processor Core ID: 1
Jul 29 18:26:31 server kernel: Dual Core AMD Opteron(tm) Processor 275 stepping 02
Jul 29 18:26:31 server kernel: CPU 1: Syncing TSC to CPU 0.
Jul 29 18:26:31 server kernel: CPU 1: synchronized TSC with CPU 0 (last diff -7 cycles, maxerr
593 cycles)
Jul 29 18:26:31 server kernel: SMP alternatives: switching to SMP code
Jul 29 18:26:32 server kernel: Booting processor 2/4 APIC 0x2
Jul 29 18:26:32 server kernel: Initializing CPU#2
Jul 29 18:26:32 server kernel: Calibrating delay using timer specific routine.. 4386.89 BogoMIPS
(lpj=2193445)
Jul 29 18:26:32 server kernel: CPU: L1 I Cache: 64K (64 bytes/line), D cache 64K (64 bytes/line)
Jul 29 18:26:32 server kernel: CPU: L2 Cache: 1024K (64 bytes/line)
Jul 29 18:26:32 server kernel: CPU 2/2 -> Node 1
Jul 29 18:26:32 server pcscd: pcscdaemon.c:507:main() pcsc-lite 1.4.4 daemon ready.
Jul 29 18:26:32 server kernel: CPU: Physical Processor ID: 1
Jul 29 18:26:32 server kernel: CPU: Processor Core ID: 0
Jul 29 18:26:32 server kernel: Dual Core AMD Opteron(tm) Processor 275 stepping 02
Jul 29 18:26:32 server kernel: CPU 2: Syncing TSC to CPU 0.
Jul 29 18:26:32 server kernel: CPU 2: synchronized TSC with CPU 0 (last diff -122 cycles, maxerr
1125 cycles)
Jul 29 18:26:32 server kernel: SMP alternatives: switching to SMP code
Jul 29 18:26:32 server kernel: Booting processor 3/4 APIC 0x3
Jul 29 18:26:32 server kernel: Initializing CPU#3
Jul 29 18:26:32 server kernel: Calibrating delay using timer specific routine.. 4386.89 BogoMIPS
(lpj=2193445)
Jul 29 18:26:32 server kernel: CPU: L1 I Cache: 64K (64 bytes/line), D cache 64K (64 bytes/line)
Jul 29 18:26:32 server kernel: CPU: L2 Cache: 1024K (64 bytes/line)
Jul 29 18:26:32 server kernel: CPU 3/3 -> Node 1
Jul 29 18:26:32 server kernel: CPU: Physical Processor ID: 1
```

Jul 29 18:26:32 server kernel: CPU: Processor Core ID: 1
Jul 29 18:26:32 server kernel: Dual Core AMD Opteron(tm) Processor 275 stepping 02
Jul 29 18:26:32 server kernel: CPU 3: Syncing TSC to CPU 0.
Jul 29 18:26:32 server kernel: CPU 3: synchronized TSC with CPU 0 (last diff -121 cycles, maxerr 1124 cycles)
Jul 29 18:26:32 server kernel: Brought up 4 CPUs
Jul 29 18:26:32 server kernel: testing NMI watchdog ... OK.

/var/log/messages (Second boot, 2.6.18-53.1.19.el5.028stab053.14)

Jul 29 18:53:25 ovz3 kernel: Booting processor 1/4 APIC 0x1
Jul 29 18:53:25 ovz3 kernel: Initializing CPU#1
Jul 29 18:53:25 ovz3 kernel: Calibrating delay using timer specific routine.. 4386.89 BogoMIPS (lpj=2193445)
Jul 29 18:53:25 ovz3 kernel: CPU: L1 I Cache: 64K (64 bytes/line), D cache 64K (64 bytes/line)
Jul 29 18:53:25 ovz3 kernel: CPU: L2 Cache: 1024K (64 bytes/line)
Jul 29 18:53:25 ovz3 kernel: CPU 1/1 -> Node 0
Jul 29 18:53:25 ovz3 kernel: CPU: Physical Processor ID: 0
Jul 29 18:53:25 ovz3 kernel: CPU: Processor Core ID: 1
Jul 29 18:53:25 ovz3 kernel: Dual Core AMD Opteron(tm) Processor 275 stepping 02
Jul 29 18:53:25 ovz3 kernel: CPU 1: Syncing TSC to CPU 0.
Jul 29 18:53:25 ovz3 kernel: CPU 1: synchronized TSC with CPU 0 (last diff 2 cycles, maxerr 592 cycles)
Jul 29 18:53:25 ovz3 kernel: Booting processor 2/4 APIC 0x2
Jul 29 18:53:25 ovz3 kernel: Initializing CPU#2
Jul 29 18:53:25 ovz3 kernel: Calibrating delay using timer specific routine.. 4386.89 BogoMIPS (lpj=2193446)
Jul 29 18:53:25 ovz3 kernel: CPU: L1 I Cache: 64K (64 bytes/line), D cache 64K (64 bytes/line)
Jul 29 18:53:25 ovz3 kernel: CPU: L2 Cache: 1024K (64 bytes/line)
Jul 29 18:53:25 ovz3 kernel: CPU 2/2 -> Node 1
Jul 29 18:53:25 ovz3 kernel: CPU: Physical Processor ID: 1
Jul 29 18:53:25 ovz3 kernel: CPU: Processor Core ID: 0
Jul 29 18:53:25 ovz3 kernel: Dual Core AMD Opteron(tm) Processor 275 stepping 02
Jul 29 18:53:25 ovz3 kernel: CPU 2: Syncing TSC to CPU 0.
Jul 29 18:53:25 ovz3 kernel: CPU 2: synchronized TSC with CPU 0 (last diff -121 cycles, maxerr 1127 cycles)
Jul 29 18:53:25 ovz3 kernel: Booting processor 3/4 APIC 0x3
Jul 29 18:53:25 ovz3 kernel: Initializing CPU#3
Jul 29 18:53:25 ovz3 kernel: Calibrating delay using timer specific routine.. 4386.89 BogoMIPS (lpj=2193447)
Jul 29 18:53:25 ovz3 kernel: CPU: L1 I Cache: 64K (64 bytes/line), D cache 64K (64 bytes/line)
Jul 29 18:53:25 ovz3 kernel: CPU: L2 Cache: 1024K (64 bytes/line)
Jul 29 18:53:25 ovz3 kernel: CPU 3/3 -> Node 1
Jul 29 18:53:26 ovz3 kernel: CPU: Physical Processor ID: 1
Jul 29 18:53:26 ovz3 kernel: CPU: Processor Core ID: 1
Jul 29 18:53:26 ovz3 kernel: Dual Core AMD Opteron(tm) Processor 275 stepping 02
Jul 29 18:53:26 ovz3 kernel: CPU 3: Syncing TSC to CPU 0.

Jul 29 18:53:26 ovz3 kernel: CPU 3: synchronized TSC with CPU 0 (last diff -122 cycles, maxerr 1127 cycles)
Jul 29 18:53:26 ovz3 kernel: Brought up 4 CPUs
Jul 29 18:53:26 ovz3 kernel: testing NMI watchdog ... CPU#0: NMI appears to be stuck (0->0)!

```
[root@ovz3 ~]# cat /proc/cpuinfo
```

```
processor      : 0
vendor_id     : AuthenticAMD
cpu family    : 15
model         : 33
model name    : Dual Core AMD Opteron(tm) Processor 275
stepping      : 2
cpu MHz       : 2193.812
cache size    : 1024 KB
physical id   : 0
siblings      : 2
core id       : 0
cpu cores     : 2
fpu           : yes
fpu_exception : yes
cpuid level   : 1
wp            : yes
flags         : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush
mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt lm 3dnowext 3dnow pn1 lahfh_lm cmp_legacy
bogomips      : 1994.04
TLB size      : 1024 4K pages
clflush size  : 64
cache_alignment : 64
address sizes  : 40 bits physical, 48 bits virtual
power management: ts fid vid ttp
```

```
processor      : 1
vendor_id     : AuthenticAMD
cpu family    : 15
model         : 33
model name    : Dual Core AMD Opteron(tm) Processor 275
stepping      : 2
cpu MHz       : 2193.812
cache size    : 1024 KB
physical id   : 0
siblings      : 2
core id       : 1
cpu cores     : 2
fpu           : yes
fpu_exception : yes
cpuid level   : 1
```

wp : yes
flags : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush
mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt lm 3dnowext 3dnow pni lahf_lm cmp_legacy
bogomips : 1994.04
TLB size : 1024 4K pages
clflush size : 64
cache_alignment : 64
address sizes : 40 bits physical, 48 bits virtual
power management: ts fid vid ttp

processor : 2
vendor_id : AuthenticAMD
cpu family : 15
model : 33
model name : Dual Core AMD Opteron(tm) Processor 275
stepping : 2
cpu MHz : 2193.812
cache size : 1024 KB
physical id : 1
siblings : 2
core id : 0
cpu cores : 2
fpu : yes
fpu_exception : yes
cpuid level : 1
wp : yes
flags : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush
mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt lm 3dnowext 3dnow pni lahf_lm cmp_legacy
bogomips : 1994.04
TLB size : 1024 4K pages
clflush size : 64
cache_alignment : 64
address sizes : 40 bits physical, 48 bits virtual
power management: ts fid vid ttp

processor : 3
vendor_id : AuthenticAMD
cpu family : 15
model : 33
model name : Dual Core AMD Opteron(tm) Processor 275
stepping : 2
cpu MHz : 2193.812
cache size : 1024 KB
physical id : 1
siblings : 2
core id : 1
cpu cores : 2
fpu : yes

fpu_exception : yes
cpuid level : 1
wp : yes
flags : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush
mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt lm 3dnowext 3dnow pn1 lahf_lm cmp_legacy
bogomips : 1994.04
TLB size : 1024 4K pages
clflush size : 64
cache_alignment : 64
address sizes : 40 bits physical, 48 bits virtual
power management: ts fid vid ttp
