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Subject: Re: [-mm PATCH 8/9] Memory controller add switch to control what type of pages to limit (v4)

Posted by [Balbir Singh](#) on Mon, 13 Aug 2007 06:17:03 GMT

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YAMAMOTO Takashi wrote:

>> YAMAMOTO Takashi wrote:

>>>> Choose if we want cached pages to be accounted or not. By default both  
>>>> are accounted for. A new set of tunables are added.

>>>>

>>>> echo -n 1 > mem\_control\_type

>>>>

>>>> switches the accounting to account for only mapped pages

>>>>

>>>> echo -n 2 > mem\_control\_type

>>>>

>>>> switches the behaviour back

>>> MEM\_CONTAINER\_TYPE\_ALL is 3, not 2.

>>>

>> Thanks, I'll fix the comment on the top.

>>

>>> YAMAMOTO Takashi

>>>

>>>> +enum {

>>>> + MEM\_CONTAINER\_TYPE\_UNSPEC = 0,

>>>> + MEM\_CONTAINER\_TYPE\_MAPPED,

>>>> + MEM\_CONTAINER\_TYPE\_CACHED,

>

> what's MEM\_CONTAINER\_TYPE\_CACHED, btw?

> it seems that nothing distinguishes it from MEM\_CONTAINER\_TYPE\_MAPPED.

>

The important types are currently MEM\_CONTAINER\_TYPE\_MAPPED and  
MEM\_CONTAINER\_TYPE\_ALL.

I added the types for future use (may be for finer accounting of types, later).

> YAMAMOTO Takashi

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Warm Regards,  
Balbir Singh  
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IBM, ISTL

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Containers mailing list  
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