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Subject: broken timer on VIA K8T800 chipset?

Posted by [vaverin](#) on Thu, 02 Aug 2007 07:28:02 GMT

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Vasily Averin wrote:

> john stultz wrote:

>> On 7/29/07, Vasily Averin <[vvs@sw.ru](mailto:vvs@sw.ru)> wrote:

>>> I've investigated why my testnode freezes. When I found that node is freezed

>>> again I've started to press Sysrq keys and noticed the following negative time jump.

I've found the reason of timer-related issue on the node.

I've noticed that number of timer interrupts is not changed (irq 0 in /proc/interrupts). Timer interrupt is not works, jiffies is not not incremented and timers cannot awake.

xtime and wall\_to\_monotonic are not changed, but ktime\_get() returns non-constant values because of \_\_get\_realtime\_clock\_ts corrects nanoseconds. Correction value is different for various clocksources: for "tsc" clocksource it is more noticeable (~4400 sec on my node). Obviously for other clocksources correction value is not too big.

When correction value overflows we can observe negative time jump.

On the other hand timers are not critical for schedulers work, it uses readtsc for timestamps and therefore node is not hangs completely.

However Now I have new question: why timer can hangs on VIA K8T800 chipset? How is possible to fix or at least workaround this issue?

I've try to disable irqbalance -- probably it will make the node more stable.

thank you,  
Vasily Averin

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